

Electronic Patent Application Fee Transmittal

Application Number:	10050233			
Filing Date:	16-Jan-2002			
Title of Invention:	DELAY LOCKED LOOP SYNTHESIZER WITH MULTIPLE OUTPUTS AND DIGITAL MODULATION			
First Named Inventor:	Robert E. Stengel			
Filer:	Barbara R. Doutre/Vernice Freebourn			
Attorney Docket Number:	CM03359J			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1400	1400
Publication fee for republication	1505	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1700